

Giada NI-Z8350UL Quick Installation Guide-V1.0

Tips:

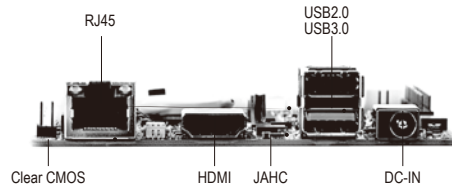
How to identify the first pin of the jumpers and connectors

- 1.The first pin is marked as *1 or solder pad or bold lines or triangular symbols
- 2.The red line on the cable or other marks show that they should be connected with the first pin of the socket.

Warning !

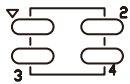
Please adopt appropriate screw and proper installation methods (including board allocation, CPU and heat sink installation, etc); otherwise, the board may be damaged.

Back Panel Interface:



Connects:

1. PWRBTN



| Pin# | Definition | Pin# | Definition |
|------|------------|------|------------|
| 1 | PBTN_MCU | 2 | GND |
| 3 | PLED+ | 4 | PLED- |

2.24Pin_MUX



| Pin# | Definition | Pin# | Definition |
|------|------------|------|--------------|
| 1 | USB_DN2 | 2 | +5V |
| 3 | USB_DP2 | 4 | +5V |
| 5 | GND | 6 | GND |
| 7 | SDA_AUDIO | 8 | GPS_TXD |
| 9 | SCL_AUDIO | 10 | GPS_RXD |
| 11 | SDA | 12 | GPS_RTS |
| 13 | SCL | 14 | GPS_CTS |
| 15 | GND | 16 | PMC_PLT_CLK4 |
| 17 | PWM0 | 18 | I2S_1_CLK |
| 19 | PWM1 | 20 | I2S_1_FS |
| 21 | GPIO_DFX4 | 22 | I2S_1_TXD |
| 23 | GPIO_N8 | 24 | I2S_1_RXD |

3.F_AUDIO (Front Panel Audio Header)

Be used connect to the second line-out and MIC in jacks that are at the front panel of you system



| Pin# | Definition | Pin# | Definition |
|------|------------|------|------------|
| 1 | LINE_R_CN | 2 | MIC_R_CN |
| 3 | GND | 4 | GND |
| 5 | GND | 6 | GND |
| 7 | LINE_L_CN | 8 | MIC_L_CN |

4.Speaker



| Pin# | Definition |
|------|------------|
| 1 | SPO_RN |
| 2 | SPO_RP |
| 3 | SPO_LP |
| 4 | SPO_LN |

5.UART(RS232 Header)

Be used with modems, serial printers, remote display terminals and serial devices

| Pin# | Definition |
|------|------------|
| 1 | GND |
| 2 | NRXD1 |
| 3 | NTXD1 |
| 4 | +5V |

11. F_USB (Front-end USB2.0 Pin)

Be used extension the front USB ports



| Pin# | Definition |
|------|----------------|
| 1 | GND |
| 2 | USB_BACK1_DN_O |
| 3 | USB_BACK1_DP_O |
| 4 | VCC_USB |

10.M-PCIE(Mini-PCIE expended slots)

Mini-PCIE only support 3G, for example, can use 3G module

9.TF Cardreader

Be used extension the TF Card

8.INVT

LVDS MENU Adjustment control interface



| Pin# | Definition |
|------|------------|
| 1 | VIN_LVDS_F |
| 2 | VIN_LVDS_F |
| 3 | BKLT_EN |
| 4 | BKLT_PWM |
| 5 | GND |
| 6 | GND |

7. LVDS LVDS(Dual-channel 24 bit LVDS Header)



| Pin# | Definition | Pin# | Definition |
|------|---------------|------|---------------|
| 1 | LVDS_PWR | 2 | LVDS_PWR |
| 3 | LVDS_TX_CLKAP | 4 | LVDS_TX_CLKBP |
| 5 | LVDS_TX_CLKAN | 6 | LVDS_TX_CLKBN |
| 7 | GND | 8 | GND |
| 9 | LVDS_TX_A0P | 10 | LVDS_TX_B0P |
| 11 | LVDS_TX_A0N | 12 | LVDS_TX_B0N |
| 13 | GND | 14 | GND |
| 15 | LVDS_TX_A1P | 16 | LVDS_TX_B1P |
| 17 | LVDS_TX_A1N | 18 | LVDS_TX_B1N |
| 19 | GND | 20 | GND |
| 21 | LVDS_TX_A2P | 22 | LVDS_TX_B2P |
| 23 | LVDS_TX_A2N | 24 | LVDS_TX_B2N |
| 25 | GND | 26 | GND |
| 27 | LVDS_TX_A3P | 28 | LVDS_TX_B3P |
| 29 | LVDS_TX_A3N | 30 | LVDS_TX_B3N |

6. LVDS_PWR_SEL LVDS Power Select jumper

| Pin# | Definition |
|------|------------|
| 1-2 | 5V |
| 3-4 | 12V |