

# Giada AP23-E3930 Quick Installation Guide-V1.0

## Tips:

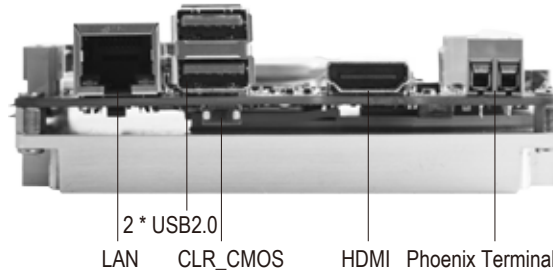
How to identify the first pin of the jumpers and connectors

1. The first pin is marked as "1" or solder pad or bold lines or triangular symbols
2. The red line on the cable or other marks show that they should be connected with the first pin of the socket.

## Warning!

Please adopt appropriate screw and proper installation methods(including board allocation,CPU and heat sink installation,etc),otherwise the board may damaged.

## Back Panel Interface:



### 11. CLR\_CMOS

Pin#	Definition
1	GND
2	CLR_CMOS

If you encounter the following:

- a) CMOS data becomes corrupted.
- b) You forgot the supervisor or user password.

You can reconfigure the system with the default values stored in the ROM BIOS.

To load the default values stored in the ROM BIOS, please follow the steps below:

1. Power-off the system and unplug the power cord.
2. Short Pin1 and Pin2 for 3-5 seconds, then back to default setting.
3. Plug the power cord and power on the system.

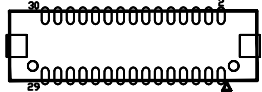
### 7. SATA0-1(Serial ATA flat-cable sockets)

Black color socket is SATAII

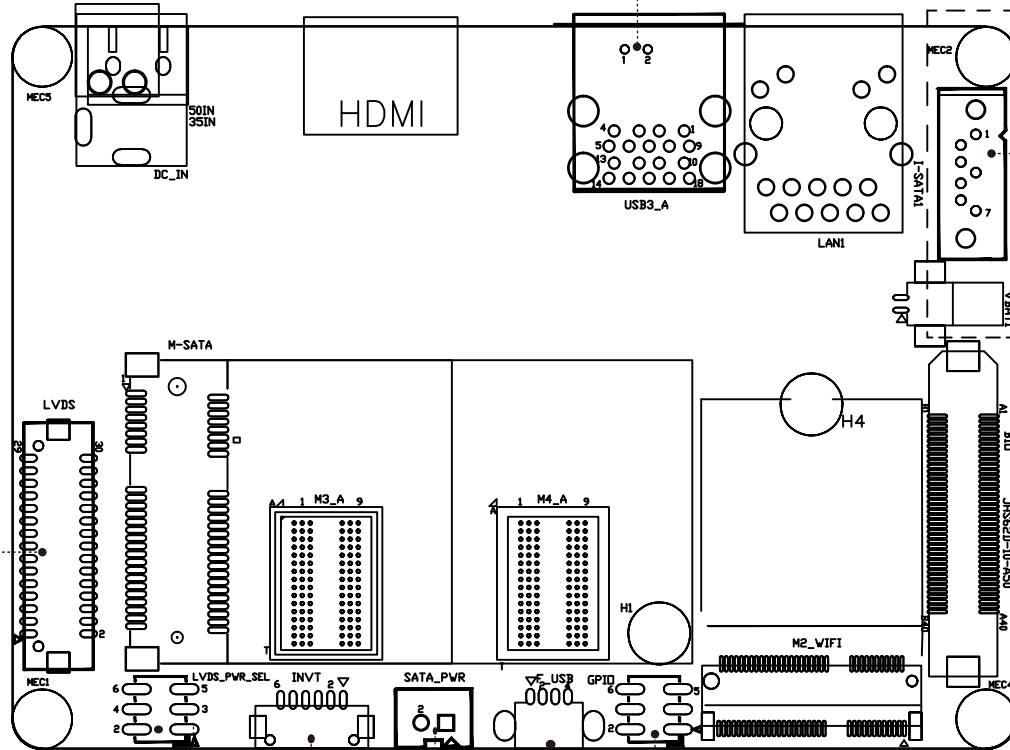
Pin#	Definition	Pin#	Definition
1	GND	6	SATA_RXP0
2	SATA_TXP0	7	GND
3	SATA_TXN0	8	GND
4	GND	9	GND
5	SATA_RXN0		

## Connectors and Jumpers:

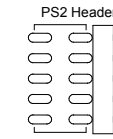
### 8. LVDS (Dual-channel 24 bit LVDS Header)



Pin#	Definition	Pin#	Definition
1	LVDS_PWR	2	LVDS_PWR
3	LVDS_TX_CLKAP	4	LVDS_TX_CLKBP
5	LVDS_TX_CLKAN	6	LVDS_TX_CLKBN
7	GND	8	GND
9	LVDS_TX_A0P	10	LVDS_TX_B0P
11	LVDS_TX_A0N	12	LVDS_TX_B0N
13	GND	14	GND
15	LVDS_TX_A1P	16	LVDS_TX_B1P
17	LVDS_TX_A1N	18	LVDS_TX_B1N
19	GND	20	GND
21	LVDS_TX_A2P	22	LVDS_TX_B2P
23	LVDS_TX_A2N	24	LVDS_TX_B2N
25	GND	26	GND
27	LVDS_TX_A3P	28	LVDS_TX_B3P
29	LVDS_TX_A3N	30	LVDS_TX_B3N

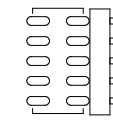


### 2. F\_COM2 (Front COM)



PIN#	Definition	PIN#	Definition
1	DCD2	2	RXD2
3	TXD2	4	DTR2
5	GND2	6	DSR2
7	RTS2	8	CTS2
9	RI2		

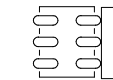
### 1. F\_COM1 (Front COM)



PIN#	Definition	PIN#	Definition
1	DCD1	2	RXD1
3	TXD1	4	DTR1
5	GND1	6	DSR1
7	RTS1	8	CTS1
9	RI1		

### 5. F\_PANEL(Front-end control panel)

Be used connect the power switch, reset switch, chassis intrusion switch/sensor and system status indicator on the chassis.



PIN#	Definition	PIN#	Definition
1	PSW	2	+PLED
3	GND	4	+HDLED
5	RESET	6	GND

### 6. LVDS\_PW LVDS Power Select jumper

Pin#	Definition
1~2	5V
3~4	3.3V
5~6	12V

### 9. LVDS INVT

LVDS MENU Adjustment control interface

Pin#	Definition
1	VIN_LVDS_F
2	VIN_LVDS_F
3	BKLT_PWM
4	BKLT_EN
5	GND
6	GND

### 10. SATA\_PW SATA Power supply

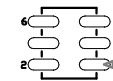
Pin#	Definition
1	+5V
2	GND

### 3. F\_USB(Front-end USB2.0 Pin)

Be used extension the front USB ports.

Pin#	Definition
1	+5V
2	USB2_0_N0
3	USB2_0_P0
4	GND

### 4. GPIO\_CON



Pin#	Definition	Pin#	Definition
1	+3.3V	2	GND
3	GPIO10	4	GPIO11
5	GPIO12	6	GPIO13